

Class	Subject
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**ISSUE CLASSIFICATION**

PATENT NUMBER

**U.S. UTILITY Patent Application**

<p>I.P.E.</p> <p>SCANNED <i>Am</i><sup>3</sup> Q.A. <i>DA</i></p>	<p>PATENT DATE</p>
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TITLE	APPLICANTS	APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
		09/732240		257	777	2815 <del>1557</del>	Chris
		Thomas Ference Wayne Howell					
		2815					
Multi-chip stack and method of fabrication utilizing self-aligning electrical contact array							
PTO-2040 12/89							

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drawg.    Figs. Drawg.    Print Fig.			<b>CLAIMS ALLOWED</b>		
				Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>		
				_____ (Date)		
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b>		
				Amount Due	Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)    _____ (Date)			<b>ISSUE BATCH NUMBER</b>		
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